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**MODEL PCIe-IIRO-16  
PCI EXPRESS 32-CHANNEL ISOLATED  
INPUT AND RELAY OUTPUT CARD  
USER MANUAL**

FILE: PCIe-IIRO-16.B4c

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## Chapter 1: Introduction

This card provides an isolated input and output interface for PCI Express Bus computers. The card has sixteen optically-isolated inputs and sixteen electro-mechanical relay outputs for monitoring and controlling DC or AC signals. An interrupt can be generated when any of the inputs change state to free up computer resources by eliminating the need for constant polling. Fully sealed, low-resistance signal relays, ideal for use in telecommunications, security and computer peripheral equipment, are used. The card occupies sixteen consecutive 8-bit registers in I/O address space.

This PCI Express version of the IIRO-16 is 100% register compatible with our PCI card version. Existing software applications should continue to magically work without changing one line of code (the device ID changes). It couldn't be easier!

### Inputs

**ISOLATED INPUTS:** The sixteen pairs of inputs can be driven by either AC or DC signals and are not polarity sensitive. Input signals are rectified by a diode bridge and applied to the input of an opto-isolator. A 1.8k $\Omega$  resistor in series provides current limiting. Standard 12/24 VAC transformer outputs can be accepted as well as DC voltages. The filter must be selected for AC inputs in order to eliminate the on/off response to AC. The voltage range to read an input as active / on is 3 to 31 volts (RMS). External resistors connected in series with the board's inputs may be used to extend the input voltage range.

**FILTERS:** Each input circuit contains a software controlled filter that has a 5 millisecond time constant. For AC inputs the filter must be selected in order to maintain a DC level at the input register. The filter is also valuable for use with slow DC input signals in a noisy environment. All filters are turned off at power-on or RESET and are then enabled or disabled by software. The on/off status of the filters can also be read by software. Note that all 16 filters are on or off together.

**INTERRUPTS:** When enabled by software the card asserts an interrupt on the PCIe-Bus when any of the sixteen isolated inputs change state from either high to low, or low to high in DC mode and from signal to no signal in AC mode. This interrupt capability is disabled at power on. Software commands are used to enable, disable, clear and read status of the interrupt circuit.

### Outputs

The sixteen electro-mechanical relay outputs are comprised of ten form C SPDT type and six form A SPST (normally open) type. The relay contacts are rated to switch 1A at 24VDC, and 0.5A at 125VAC. All the relays are turned off at power-on, and are then controlled by data bits written by software and latched. The on/off status of the relays can be read back by software.

## Specification

### Isolated Inputs

- Number of inputs: Sixteen
- Type: Non-polarized, optically isolated from each other and from the computer
- Input voltage range: 3 to 31V DC or AC RMS (40 to 10kHz)
- Isolation\*: 500V
- Input Resistance: 1.8k ohms in series with a photo-coupler diode
- Response Time: 5 mSec w/filter
- Interrupts: PCIe-INTA

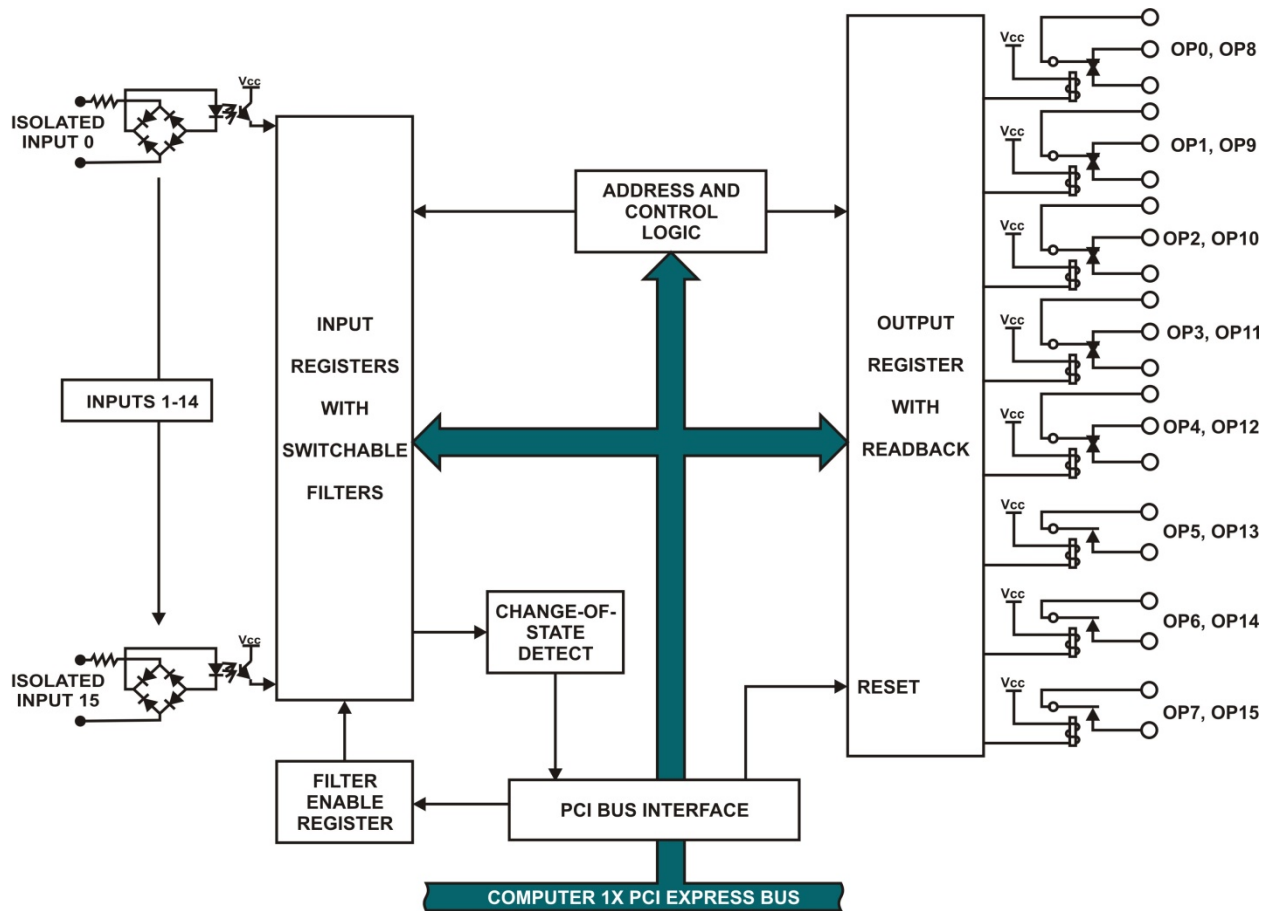
### Relay Outputs

- Number of outputs: 16
- Max Switching Current: 1 A
- Contact Rating: 2 A carry current, bifurcated, gold clad, Au-alloy
- Contact Arrangement: Channels 0-4 and 8-12 are SPDT Form C; channels 5-7 and 13-15 are SPST Form A.
- Contact Rating: Initial 100 mΩ maximum
- Contact Life: mech'l: 5 million operations minimum; elect'l: 5 million operations minimum at full load
- Operate time: 5 mSec max
- Release Time: 5 mSec max
- Isolation\*: 1000V
- Vibration: 10 to 55 Hz, 3.3 mm dbl amp; mech. dur. 1,000 m/s<sup>2</sup> (approx 100G)

\* Notes on Isolation: Opto-Isolators, connector and relays are rated for at least 500V, but isolation voltage breakdowns will vary and is affected by factors like cabling, spacing of pins, spacing between traces on PCB, humidity, dust and other environmental factors. This is a safety issue so a careful approach is required. For CE certification, isolation was specified at 40V AC and 60V DC. The design intention was to eliminate the influence of common mode. Use proper wiring techniques to minimize voltage between channels and to ground. For example, when working with AC lines do not connect the hot side of the line to an input.

### Environmental

- Ambient Temperature: Operating: 0 °C. to +70 °C  
Storage: -40 °C. to +85 °C
- I/O Connector: DB78 Female on Card Mounting Bracket
- Humidity: 5 to 95% (non-condensing)
- Weight: Approx. 8 oz
- Size: 6.80" long (173 mm)



**Figure 1-1: Block Diagram**

## Chapter 2: Installation

### Software Installation

The software provided with this board is available by request on CD for a fee, or downloaded via the product page for free and must be installed onto your hard disk prior to use.

### Installing from Downloaded Installer

Download the software package here: <http://accesio.com/files/packages/PCIe-IIRO-16%20Install.exe> or it can be found on the Manuals / Software tab on the product page. Run the downloaded installer which launches the Install Wizard. Follow the on-screen prompts to complete the software installation. When you see “Installation complete!” you will have options to “Show Samples”, “Run Settings...” or “Finish”.

### Installing from CD

Perform the following steps as appropriate for your operating system. Substitute the appropriate drive letter for your drive where you see D: in the examples below.

#### Windows

- a. Place the CD into your CD-ROM drive.
- b. The CD should automatically run the install program. If the install program does not run, click START | RUN and type D: INSTALL, click OK or press ENTER.
- c. Follow the on-screen prompts to install the software for this board.

#### Linux

Please refer to linux.htm, and visit <https://github.com/accesio> for more.

#### **Caution! \* ESD**

***A single static discharge can damage your card and cause premature failure! Please follow all reasonable precautions to prevent a static discharge such as grounding yourself by touching any grounded surface **prior to touching the card.*****



## Hardware Installation

1. Do not install card into the computer until the software has been fully installed.
2. Turn OFF computer power AND unplug AC power from the system.
3. Remove the computer cover.
4. Carefully install the card in an available PCIe expansion slot (you may need to remove a backplate first).
5. Inspect for proper fit of the card and tighten the screw. Make sure that the card mounting bracket is properly screwed into place and that there is a positive chassis ground.
6. Install an I/O cable onto the card's bracket mounted connector.
7. Replace the computer cover and turn ON the computer which should auto-detect the card (depending on the operating system) and automatically finish installing the drivers.
8. Run PCIFind.exe to complete installing the card into the registry (for Windows only) and to determine the assigned resources.
9. Run one of the provided sample programs that was copied to the newly created card directory (from the CD or downloaded installer) to test and validate your installation.

The base address assigned by BIOS or the operating system can change each time new hardware is installed into or removed from the computer. Please recheck PCIFind or Device Manager if the hardware configuration is changed. Software you write can automatically determine the base address of the card using a variety of methods depending on the operating system. In DOS, the PCI\SOURCE directory shows the BIOS calls used to determine the address and IRQ assigned to installed PCIe devices. In Windows, the Windows sample programs demonstrate querying the registry entries (created by PCIFind and NTIOPCI.SYS during boot-up) to determine this same information.

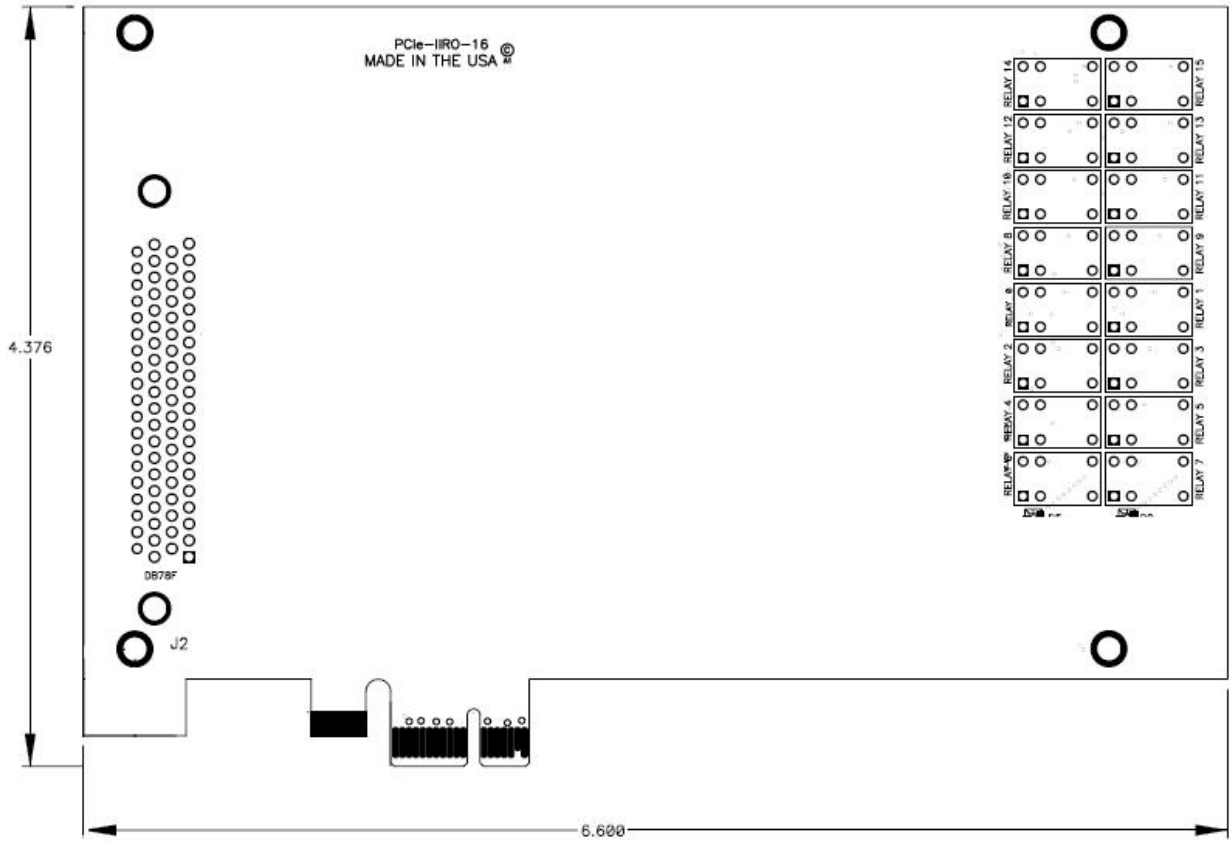


Figure 2-1: Dimensional Drawing

## Chapter 3: Address Selection

This card uses I/O addresses offset from the base address assigned by the PCIe bus. The address spaces are defined in the programming section of this manual.

PCIe architecture is Plug-and-Play. This means that the BIOS or Operating System determines the resources assigned to PCIe cards rather than the user selecting those resources with switches or jumpers. As a result, you cannot set or change the card's base address or IRQ level. You can only determine what the system has assigned.

To determine the base address that has been assigned, run the PCIFind utility program. This utility will display a list of all the cards detected on the PCIe bus, the addresses assigned to each function on each of the cards, and the respective IRQs.

Alternatively, Windows systems can be queried to determine which resources were assigned. In these operating systems, you can use either PCIFind, or the Device Manager utility from the System Properties Applet of the control panel. The card is installed in the Data Acquisition class of the Device Manager list. Selecting the card, clicking Properties, and then selecting the Resources Tab will display a list of the resources allocated to the card.

The PCIe bus supports 64K of I/O address space, so your card's addresses may be located anywhere in the 0000h to FFFFh range. The card occupies sixteen consecutive 8 bit registers in I/O address space.

PCIFind uses the Vendor ID and Device ID to search for your card, then reads the base address and IRQ. If you want to determine the base address and IRQ without using PCIFind, use the following information:

The Vendor ID code is 494F (ASCII for "I/O")

The Device ID code for the card is 0F09

An example of how to locate PCIe card resources is provided with in the PCI\SOURCE directory, under your installation directory. This code runs in DOS, and uses the PCIe defined interrupt BIOS calls to query the PCIe bus for card specific information. You will need the Device ID and Vendor ID listed above to use this code.

## Chapter 4: Programming

The base or starting address is assigned by the computers plug-and-play operating system during installation and will fall on an eight byte boundary (see chapter 3).

The card's read and write functions are as follows:

| I/O Address | Read               | Write              |
|-------------|--------------------|--------------------|
| Base +0     | Outputs 0 – 7      | Outputs 0 – 7      |
| Base +1     | Inputs 0 – 7       | Interrupt Clear    |
| Base +2     | Interrupt Enable   | Interrupt Disable  |
| Base +3     | Filter Enable      | Filter Disable     |
| Base +4     | Outputs 8 – 15     | Outputs 8 – 15     |
| Base +5     | Inputs 8 – 15      | Unused             |
| Base +6     | Interrupt Status   | Unused             |
| Base +7     | Filter Status      | Software Reset     |
| Base +8     | COS/IRQ State 0-7  | COS/IRQ Clear 0-7  |
| Base +9     | COS/IRQ State 8-15 | COS/IRQ Clear 8-15 |
| Base +A-F   | Unused             | Unused             |

**Table 4-1:** Register Address Map

### Base +0 and Base +4 Relay Outputs

At power-up, all relays are de-energized (normally-closed contacts). The relay outputs are controlled by writing to base address +0 and +4. Each bit within the registers controls a specific relay. A "1" energizes the corresponding relay and a "0" de-energizes it. The status of the relays can be determined at any time by a READ from base address +0 and +4.

| Bit Position    | D7   | D6   | D5   | D4   | D3   | D2   | D1  | D0  |
|-----------------|------|------|------|------|------|------|-----|-----|
| Relay Low Byte  | OP7  | OP6  | OP5  | OP4  | OP3  | OP2  | OP1 | OP0 |
| Relay High Byte | OP15 | OP14 | OP13 | OP12 | OP11 | OP10 | OP9 | OP8 |

For example, if bit D5 is set to a 1 by writing 20 hex to the base address +0 the relay that controls OP5 is energized, closing the associated normally-open contacts. All other relays would be de-energized and their normally-closed contacts would be closed.

## Base +1 and Base +5 Isolated Inputs

Input states can be read from base address +1 and +5. Each of the bits within the bytes corresponds to a particular isolated input. A "1" signifies that the input is energized (+/- current flow) and a "0" signifies that the input is de-energized.

| Bit Position    | D7   | D6   | D5   | D4   | D3   | D2   | D1  | D0  |
|-----------------|------|------|------|------|------|------|-----|-----|
| Input Low Byte  | IP7  | IP6  | IP5  | IP4  | IP3  | IP2  | IP1 | IP0 |
| Input High Byte | IP15 | IP14 | IP13 | IP12 | IP11 | IP10 | IP9 | IP8 |

## Base +2 IRQ Enable/Disable and Base +6 IRQ Status

| Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2       | Bit 1       | Bit 0       |
|-------|-------|-------|-------|-------|-------------|-------------|-------------|
| '0'   | '0'   | '0'   | '0'   | '0'   | IRQ pending | IRQ enabled | IRQ enabled |

### Base +6, COS Enable

**Interrupts:** The card supports interrupts. The interrupt level is assigned by the plug-and-play operating system. The assigned interrupt level can be viewed when PCIFind.EXE is run (see chapter 3). The card's interrupt capability makes it unnecessary to continuously poll inputs (reading base address +1 and +5) to detect when an input state has changed. To enable interrupts read base address +2 (the value is meaningless). To disable interrupts, write any value to base address +2. To clear an interrupt write any value to base address +1. The user must include the clear instruction in the interrupt service routine software. For shared interrupt applications the card has an IRQ status byte that can be read from base address +6. Bits 0 and 1 SET indicate interrupts are enabled, bit 2 SET indicates an IRQ has been generated, bits 3 thru 7 will always read 0.

### Base +3 Filters (Read/Write) and Base +7 Filters (Read)

Sometimes it is necessary to slow down the card's response to eliminate noise spikes on DC inputs in industrial environments or to convert sinusoidal AC inputs. The 5 mSec filter can be enabled for all inputs by a software read from base address +3 or disabled by a write of any value to base address +3. The status of the filters can be read back from base address +7. A value of FF hex indicates the filters are active and a value of 00 hex indicates they are disabled.

|       |       |       |       |       |       |       |       |
|-------|-------|-------|-------|-------|-------|-------|-------|
| Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
| 'x'   | 'x'   | 'x'   | 'x'   | 'x'   | 'x'   | 'x'   | 'x'   |

### Base +7 Software Reset (Write)

|       |       |       |       |       |       |       |       |
|-------|-------|-------|-------|-------|-------|-------|-------|
| Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
| '0'   | '0'   | '0'   | '0'   | '0'   | '0'   | '0'   | X     |

To reset all register values to default state, write to base +7 with bit 0 high

### Base +8 and Base +9 Change of State Registers

Read the 16 bit register at base+8 to see what input lines have changed. If interrupts were enabled by a READ of base+2 and if any of the bits in the register at base+8 are SET then an IRQ will be generated. Write the value that was read from the register at base+8 back to base+8 to clear only those bits that are HIGH. If the COS status register is zeroed by a WRITE then the IRQ will be cleared. Events that happen while the register is being accessed will not be missed.

| Bit Position    | D7    | D6    | D5    | D4    | D3    | D2    | D1   | D0   |
|-----------------|-------|-------|-------|-------|-------|-------|------|------|
| Input Low Byte  | COS7  | COS6  | COS5  | COS4  | COS3  | COS2  | COS1 | COS0 |
| Input High Byte | COS15 | COS14 | COS13 | COS12 | COS11 | COS10 | COS9 | COS8 |

## Chapter 5: Connector Pin Assignments

Digital I/O signals are connected to the card via a female 78-pin D type connector that extends through the back of the computer case. The mating connector is an AMP 748368-1 or equivalent. We optionally provide a breakout cable that divides the 78-pin I/O connector down to two 37-pin D type connectors. See the following page for information about termination solutions.

| Pin | Name    | Function       | Pin     | Name     |
|-----|---------|----------------|---------|----------|
| 1   | IP7     | Isolated Input | 40      | IP15     |
| 2   | IP6     |                | 41      | IP14     |
| 3   | IP5     |                | 42      | IP13     |
| 4   | IP4     |                | 43      | IP12     |
| 5   | IP3     |                | 44      | IP11     |
| 6   | IP2     |                | 45      | IP10     |
| 7   | IP1     |                | 46      | IP9      |
| 8   | IP0     |                | 47      | IP8      |
| 9   | OP7(C)  | Relay Contact  | 48      | OP15(C)  |
| 10  | OP6(C)  |                | 49      | OP14(C)  |
| 11  | OP5(C)  |                | 50      | OP13(C)  |
| 12  | OP4(NC) |                | 51      | OP12(NC) |
| 13  | OP4(NO) |                | 52      | OP12(NO) |
| 14  | OP3(C)  |                | 53      | OP11(C)  |
| 15  | OP2(NC) |                | 54      | OP10(NC) |
| 16  | OP2(NO) |                | 55      | OP10(NO) |
| 17  | OP1(C)  |                | 56      | OP9(C)   |
| 18  | OP0(NC) |                | 57      | OP8(NC)  |
| 19  | OP0(NO) | 58             | OP8(NO) |          |
| 20  |         | Not Used       | 59      |          |
| 21  | IP7     | Isolated Input | 60      | IP15     |
| 22  | IP6     |                | 61      | IP14     |
| 23  | IP5     |                | 62      | IP13     |
| 24  | IP4     |                | 63      | IP12     |
| 25  | IP3     |                | 64      | IP11     |
| 26  | IP2     |                | 65      | IP10     |
| 27  | IP1     |                | 66      | IP9      |
| 28  | IP0     |                | 67      | IP8      |
| 29  | OP7(NO) | Relay Contact  | 68      | OP15(NO) |
| 30  | OP6(NO) |                | 69      | OP14(NO) |
| 31  | OP5(NO) |                | 70      | OP13(NO) |
| 32  | OP4(C)  |                | 71      | OP12(C)  |
| 33  | OP3(NC) |                | 72      | OP11(NC) |
| 34  | OP3(NO) |                | 73      | OP11(NO) |
| 35  | OP2(C)  |                | 74      | OP10(C)  |
| 36  | OP1(NC) |                | 75      | OP9(NC)  |
| 37  | OP1(NO) |                | 76      | OP9(NO)  |
| 38  | OP0(C)  |                | 77      | OP8(C)   |
| 39  |         | Not Used       | 78      |          |

**Table 5-1:** J2 DB78F Connector Pin Assignments

## Termination Solutions

The breakout solution for the card involves a “Y” cable that terminates into two identically pinned out DB37F connectors. As part of a kit, these connectors plug into two STB-37 screw terminal cards, which easily mount into a length of SNAPTRACK. The pinout of each leg when plugged into an STB-37 is identical to that of the eight channel ISA and PCI Isolated Input/Relay Output Cards. The “Y” cable is six (6) feet long on each leg.

| “1-39” Cable |         |                | “40-78” Cable |          |                |
|--------------|---------|----------------|---------------|----------|----------------|
| PIN          | NAME    | FUNCTION       | PIN           | NAME     | FUNCTION       |
| 1            | IP7     | Isolated Input | 1             | IP15     | Isolated Input |
| 2            | IP6     |                |               |          |                |
| 3            | IP5     |                |               |          |                |
| 4            | IP4     |                |               |          |                |
| 5            | IP3     |                |               |          |                |
| 6            | IP2     |                |               |          |                |
| 7            | IP1     |                |               |          |                |
| 8            | IP0     |                |               |          |                |
| 9            | OP7(C)  | Relay Contact  | 9             | OP15(C)  | Relay Contact  |
| 10           | OP6(C)  |                |               |          |                |
| 11           | OP5(C)  |                |               |          |                |
| 12           | OP4(NC) |                |               |          |                |
| 13           | OP4(NO) |                |               |          |                |
| 14           | OP3(C)  |                |               |          |                |
| 15           | OP2(NC) |                |               |          |                |
| 16           | OP2(NO) |                |               |          |                |
| 17           | OP1(C)  |                |               |          |                |
| 18           | OP0(NC) |                |               |          |                |
| 19           | OP0(NO) |                |               |          |                |
| 20           | IP7     | Isolated Input | 20            | IP15     | Isolated Input |
| 21           | IP6     |                |               |          |                |
| 22           | IP5     |                |               |          |                |
| 23           | IP4     |                |               |          |                |
| 24           | IP3     |                |               |          |                |
| 25           | IP2     |                |               |          |                |
| 26           | IP1     |                |               |          |                |
| 27           | IP0     |                |               |          |                |
| 28           | OP7(NO) | Relay Contact  | 28            | OP15(NO) | Relay Contact  |
| 29           | OP6(NO) |                |               |          |                |
| 30           | OP5(NO) |                |               |          |                |
| 31           | OP4(C)  |                |               |          |                |
| 32           | OP3(NC) |                |               |          |                |
| 33           | OP3(NO) |                |               |          |                |
| 34           | OP2(C)  |                |               |          |                |
| 35           | OP1(NC) |                |               |          |                |
| 36           | OP1(NO) |                |               |          |                |
| 37           | OP0(C)  |                |               |          |                |
|              |         |                | 37            | OP8(C)   |                |

**Table 5-2:** DB37F (x2) Breakout Cable Pin Assignments



## Customer Comments

If you experience any problems with this manual or just want to give us some feedback, please email us at: [manuals@accessio.com](mailto:manuals@accessio.com). Please detail any errors you find and include your mailing address so that we can send you any manual updates.

